

Fig. 1a

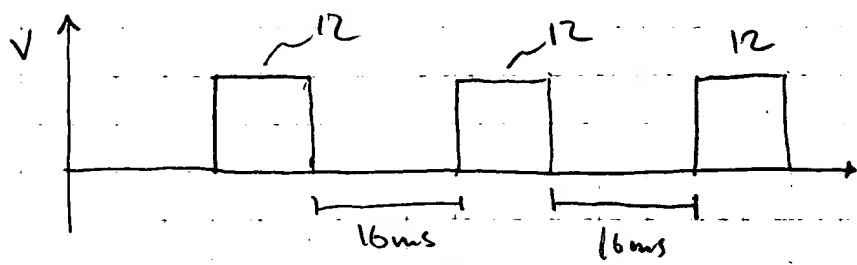


Fig. 1b

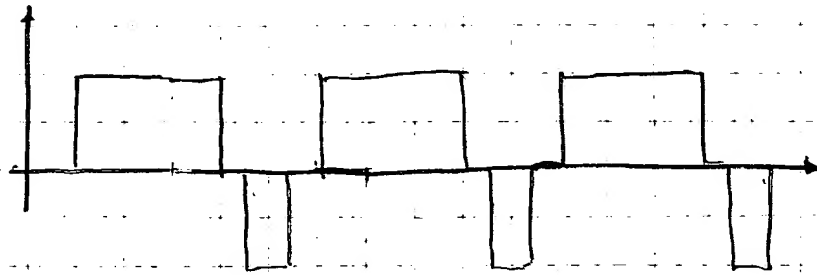


Fig. 1c

Scanned with CamScanner

Clock Name	Power Down	Energy Detect	Auto-Neg	10 Base T	100 Base TX	Serial 10 BT	FX Mode	Test Loopback	PCS bypass 10	PCS bypass 100
bt_txc_20	OFF	OFF	OFF	20	OFF	20	OFF	OFF	20	OFF
bt_rxc_20	OFF	OFF	OFF	20	OFF	20	OFF	OFF	20	OFF
tx_txc	OFF	OFF	OFF	OFF	25	OFF	25	25	OFF	OFF
tx_rxc	OFF	OFF	OFF	OFF	25	OFF	25	25	OFF	25
mii_txc	OFF	25/2.5	25	2.5	25	10	25	25	20	25
mii_rxc	OFF	OFF	OFF	2.5(a)	25(a)	10(a)	25(a)	25(a)	OFF	25
cp_arb	OFF	ON	25	25	OFF	25	OFF	OFF	OFF	OFF

Fig. 2

Signal Name	Power Down	Energy Detect	Auto-Neg	10 Base T	100 Base TX	FX Mode	Test Loopback	PCS bypass 10	PCS bypass 100
mr_pd_pll	1	1	0	0	0	0	0	0	0
mr_pd_equal	1	1	0	1	0	1	0	1	0
mr_bt_rcvr	1	1	0	0	1	1	1	0	1
mr_pd_lp	1	1	0	1	1	1	1	1	1
mr_pd_en_det	1	0	0	LINK	LINK	1	1	LINK(a)	LINK(b)
mr_pd_fx	1	1	1	1	1	0	0	1	1

Fig. 3